

IN THE CLAIMS

After granting a filing date to this continuation application, please cancel claim 1 and add new claims 10-46 as set forth below.

B2  
1--10. A pattern forming method comprising the steps of:  
preparing a semitransparent phase shifting mask including  
(a) a semitransparent phase shifting pattern formed at a predetermined position on a photomask substrate and (b) a light shielding area provided at a peripheral edge portion of said semitransparent phase shifting pattern and serving to make an intensity of light having passed through said light shielding area smaller than an intensity of light having passed through said semitransparent phase shifting film, as measured on a to-be-exposed film; and

exposing, with a projection exposure optical system, said to-be-exposed film by use of said semitransparent phase shifting mask.

2 --11. A pattern forming method ~~of~~ comprising the steps of:

preparing a semitransparent phase shifting mask including  
(a) a first semitransparent phase shifting pattern having a semitransparent phase shifting film, said phase shifting film being formed at a predetermined position on a photomask substrate and having a transmittance with respect to exposure light not higher than 25% and (b) a light shielding area provided at a peripheral edge portion of said first

semitransparent phase shifting pattern and serving to make an intensity of light having passed through said light shielding area smaller than an intensity of light having passed through said semitransparent phase shifting film, as measured on a to-be-exposed film;

preparing a substrate having a to-be-exposed film; and

exposing, with a projection exposure optical system, said to-be-exposed film to said exposure light by use of said semitransparent phase shifting mask.

3 -- ~~12~~. A method according to claim <sup>2</sup> ~~11~~, wherein said light shielding area includes a second semitransparent phase shifting pattern having a semitransparent phase shifting portion and a transparent portion, ~~said second semitransparent phase shifting pattern being comprised of a pattern beyond a critical resolution of an exposure apparatus.~~ DR(5/12/99)

4 -- ~~13~~. A method according to claim <sup>3</sup> ~~12~~, wherein a ratio  $\alpha$  of an area of said transparent portion to an area of said semitransparent phase shifting portion is defined as  $\alpha = \beta \sqrt{T}$ , where T represents a transmittance of said semitransparent phase shifting portion, and  $\beta$  represents a value in a range  $0.5 \leq \beta \leq 2.0$ . DR(5/12/99)

5 -- ~~14~~. A method according to claim <sup>2</sup> ~~11~~, wherein a light shielding portion is provided within a region for said first semitransparent phase shifting pattern, said light shielding portion serving to make an intensity of light having passed through said light shielding portion smaller than an intensity

of light having passed through said semitransparent phase shifting film, as measured on a to-be-exposed target.

--<sup>6</sup>15. A method according to claim <sup>2</sup>11, wherein a transparent pattern is provided within said light shielding area to be transferred onto said to-be-exposed film.

--<sup>7</sup>16. A method according to claim <sup>2</sup>11, wherein said first semitransparent phase shifting pattern is a pattern for forming a device.

*BT*  
--<sup>8</sup>17. A pattern forming method comprising the steps of:  
mounting a substrate having a photoresist film, on a sample stage of an aligner having a masking blade;

mounting, on a mask support of said aligner, a semitransparent phase shifting mask including (a) a semitransparent phase shifting pattern formed at a predetermined position on a photomask substrate and (b) a light shielding area provided at a peripheral edge portion of said semitransparent phase shifting pattern and serving to make an intensity of light having passed through said light shielding area smaller than an intensity of light having passed through said semitransparent phase shifting film, as measured on said photoresist film; and

exposing said photoresist film by use of said semitransparent phase shifting mask.

*Sub C*  
~~9-18. A pattern forming method comprising the steps of:  
mounting a substrate having a to-be-exposed film, on a sample stage of an aligner having a masking blade;~~

62 mounting, on a mask support of said aligner, a semitransparent phase shifting mask including (a) a first semitransparent phase shifting pattern having a semitransparent phase shifting film, said phase shifting film being formed at a predetermined position on a photomask substrate and having a transmittance with respect to exposure light not higher than 25% and (b) a light shielding area provided at a peripheral edge portion of said first semitransparent phase shifting pattern and serving to make an intensity of light having passed through said light having passed through said semitransparent phase shifting film, as measured on a to-be-exposed film;

exposing a first area of said to-be-exposed film by use of said semitransparent phase shifting mask;

moving said sample stage in a horizontal direction; and exposing a second area, different from said first area, of said to-be-exposed film by use of said semitransparent phase shifting mask.

9  
10 -- 19. A method according to claim 18, wherein said light shielding area includes a second semitransparent phase shifting pattern having a semitransparent phase shifting portion and a transparent portion, ~~said second semitransparent phase shifting pattern being comprised of a pattern beyond a critical resolution of an exposure apparatus.~~

<sup>11</sup>  
--~~20~~. A method according to claim <sup>10</sup>19, wherein a ratio  $\alpha$  of an area of said transparent portion to an area of semitransparent phase shifting portion is defined as  $\alpha = \beta\sqrt{T}$ , where T represents a transmittance of said semitransparent phase shifting portion, and  $\beta$  represents a value in a range  $0.5 \leq \beta \leq 2.0$ .

<sup>12</sup>  
--~~21~~. A method according to claim <sup>9</sup>18, wherein a light shielding portion is provided within a region for said first semitransparent phase shifting pattern, said light shielding portion serving to make an intensity of light having passed through said light shielding portion smaller than an intensity of light having passed through said semitransparent phase shifting film, as measured on a to-be-exposed film.

<sup>13</sup>  
--~~22~~. A method according to claim <sup>9</sup>18, wherein a transparent pattern is provided within said light shielding area to be transferred onto said to-be-exposed film.

<sup>14</sup>  
--~~23~~. A method according to claim <sup>9</sup>18, wherein said first semitransparent phase shifting pattern is a pattern for forming a device.

<sup>15</sup>  
--~~24~~. A method according to claim <sup>9</sup>18, wherein said second area includes a portion of said to-be-exposed film which is shielded by said light shielding area in said step of exposing said first area of said to-be-exposed film.

<sup>16</sup>  
--25. A method of manufacturing a semiconductor device, comprising the steps of:

preparing a semitransparent phase shifting mask including  
(a) a first semitransparent phase shifting pattern for formation of a semiconductor device having a semitransparent phase shifting film, said phase shifting film being formed at a predetermined position on a photomask substrate and having a transmittance with respect to exposure light not higher than 25% and (b) a light shielding area provided at a peripheral edge portion of said first semitransparent phase shifting pattern and serving to make an intensity of light having passed through said light shielding area smaller than an intensity of light having passed through said semitransparent phase shifting film, as measured on a to-be-exposed film;

preparing a substrate having a to-be-exposed film; and

exposing, with a projection exposure optical system, said to-be-exposed film to said exposure light by use of said semitransparent phase shifting mask.

<sup>17</sup>  
--26. A method according to claim <sup>16</sup>25, wherein said light shielding area includes a second semitransparent phase shifting pattern having a semitransparent phase shifting portion and a transparent portion, ~~said second semitransparent phase shifting pattern being comprised of a pattern beyond a critical resolution of an exposure apparatus.~~

<sup>18</sup>  
--~~27~~. A method according to claim <sup>17</sup>~~26~~, wherein a ratio  $\alpha$  of an area of said transparent portion to an area of semitransparent phase shifting portion is defined as  $\alpha = \beta\sqrt{T}$ , where T represents a transmittance of said semitransparent phase shifting portion, and  $\beta$  represents a value in a range  $0.5 \leq \beta \leq 2.0$ .

<sup>19</sup>  
--~~28~~. A method according to claim <sup>16</sup>~~25~~, wherein a light shielding portion is provided within a region for said first semitransparent phase shifting pattern, said light shielding portion serving to make an intensity of light having passed through said light shielding portion smaller than an intensity of light having passed through said semitransparent phase shifting film, as measured on a to-be-exposed target.

<sup>20</sup>  
--~~29~~. A method according to claim <sup>16</sup>~~25~~, wherein a transparent pattern is provided within said light shielding area to be transferred onto said to-be-exposed film.

<sup>21</sup>  
--~~30~~. A method of manufacturing a semiconductor device, comprising the steps of:

mounting a substrate having a to-be-exposed film, on a sample stage of an aligner having a masking blade;

mounting, on a mask support of said aligner, a semitransparent phase shifting mask including (a) a first semitransparent phase shifting pattern for formation of a semiconductor device having a semitransparent phase shifting film, said phase shifting film being formed at a predetermined position on a photomask substrate and having a transmittance

with respect to exposure light not higher than 25% and (b) a light shielding area provided at a peripheral edge portion of said first semitransparent phase shifting pattern and serving to make an intensity of light having passed through said light shielding area smaller than an intensity of light having passed through said semitransparent phase shifting film, as measured on a to-be-exposed film;

exposing a first area of said to-be-exposed film by use of said semitransparent phase shifting mask;

moving said sample stage in a horizontal direction; and

exposing a second area, different from said first area, of said to-be-exposed film by use of said semitransparent phase shifting mask.

<sup>22</sup> 21. A method according to claim <sup>21</sup> 30, wherein said light shielding area includes a second semitransparent phase shifting pattern having a semitransparent phase shifting portion and a transparent portion, ~~said second semitransparent phase shifting pattern being comprised of a pattern beyond a critical resolution of an exposure apparatus.~~

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<sup>23</sup> 22. A method according to claim <sup>22</sup> 31, wherein a ratio  $\alpha$  of an area of said transparent portion to an area of said semitransparent phase shifting portion is defined as  $\alpha = \beta \sqrt{T}$ , where T represents a transmittance of said semitransparent phase shifting portion, and  $\beta$  represents a value in a range  $0.5 \leq \beta \leq 2.0$ .

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<sup>24</sup>  
--~~33~~. A method according to claim <sup>22</sup>~~31~~, wherein a light shielding portion is provided within a region for said first semitransparent phase shifting pattern, said light shielding portion serving to make an intensity of light having passed through said light shielding portion smaller than an intensity of light having passed through said semitransparent phase shifting film, as measured on a to-be-exposed target.

<sup>25</sup>  
--~~34~~. A method according to claim <sup>22</sup>~~31~~, wherein a transparent pattern is provided within said light shielding area to be transferred onto said to-be-exposed film.

<sup>26</sup>  
--~~35~~. A method of manufacturing a semiconductor device, comprising the steps of:

forming an impurity-doped layer in a predetermined region of a semiconductor substrate;

forming an insulating film on said semiconductor substrate having said doped layer formed therein;

forming a photoresist film on said insulating film;

mounting said substrate having said photoresist film, on a sample stage of an aligner having a masking blade;

mounting, on a mask support of said aligner, a semitransparent phase shifting mask including (a) a first semitransparent phase shifting pattern for formation of a hole having a semitransparent phase shifting film, said phase shifting film being formed at a predetermined position on a photomask substrate and having a transmittance with respect to exposure light not higher than 25% and (b) a light shielding

area provided at a peripheral edge portion of said first semitransparent phase shifting pattern and serving to make an intensity of light having passed through said light shielding area smaller than an intensity of light having passed through said semitransparent phase shifting film, as measured on said photoresist film; and

exposing a first area of said photoresist film by use of said semitransparent phase shifting mask;

moving said sample stage in a horizontal direction;

exposing a second area, different from said first area, of said photoresist film by use of said semitransparent phase shifting mask; and, thereafter

etching said insulating film to form a hole above said impurity doped layer.

<sup>27</sup>  
--36. A method of manufacturing a semiconductor device, comprising the steps of

forming a wiring layer above a predetermined region of a semiconductor substrate;

forming an insulating film on said semiconductor substrate having said wiring layer formed thereabove;

forming a photoresist film on said insulating film;

mounting said substrate having said photoresist film, on a sample stage of an aligner having a masking blade;

mounting, on a mask support of said aligner, a semitransparent phase shifting mask including (a) a first semitransparent phase shifting pattern for formation of a hole

having a semitransparent phase shifting film, said phase shifting film being formed at a predetermined position on a photomask substrate and having a transmittance with respect to exposure light not higher than 25% and (b) a light shielding area provided at a peripheral edge portion of said first semitransparent phase shifting pattern and serving to make an intensity of light having passed through said light shielding area smaller than an intensity of light having passed through said semitransparent phase shifting film, as measured on said photoresist film; and

exposing a first area of said photoresist film by use of said semitransparent phase shifting mask;

moving said sample stage in a horizontal direction;

exposing a second area, different from said first area, of said photoresist film by use of said semitransparent phase shifting mask; and, thereafter

etching said insulating film to form a hole above said wiring layer.

--<sup>28</sup>37. A method of manufacturing a semiconductor device, comprising the steps of:

preparing a semitransparent phase shifting mask including (a) a first semitransparent phase shifting pattern for formation of a semiconductor device having a semitransparent phase shifting film, said phase shifting film being formed at a predetermined position on a photomask substrate and having a transmittance with respect to exposure light not higher than

25% and (b) a light shielding area provided at a peripheral edge portion of said first semitransparent phase shifting pattern;

preparing a substrate having a to-be-exposed film, said light shielding area of said semitransparent phase shifting mask serving to make an intensity of light having passed through said light shielding area not larger than 0.05, as measured on said to-be-exposed film; and

exposing, with a projection exposure optical system, said to-be-exposed film to said exposure light by use of said semitransparent phase shifting mask.

*29*  
--38. A method according to claim *28* wherein said light shielding area includes a second semitransparent phase shifting pattern having a semitransparent phase shifting portion and a transparent portion, ~~said second semitransparent phase shifting pattern being comprised of a pattern beyond a critical resolution of an exposure apparatus.~~

*28(5/12/95)*

*30*  
--39. A method according to claim *29*, wherein a ratio  $\alpha$  of an area of said transparent portion to an area of said semitransparent phase shifting portion is defined as  $\alpha = \beta\sqrt{T}$ , where T represents a transmittance of said semitransparent phase shifting portion, and  $\beta$  represents a value in a range  $0.5 \leq \beta \leq 2.0$ .

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*31*  
--40. A method according to claim *31*, wherein a light shielding portion is provided with a region for said first semitransparent phase shifting pattern, said light shielding

*34*

portion serving to make an intensity of light having passed through said light shielding portion smaller than an intensity of light having passed through said semitransparent phase shifting film, as measured on a to-be-exposed target.

<sup>32</sup>  
--41. A method according to claim <sup>28</sup>37, wherein a transparent pattern is provided within said light shielding area to be transferred onto said to-be-exposed film.

<sup>33</sup>  
--42. A method of manufacturing a semiconductor device, comprising the steps of:

mounting a substrate having <sup>a</sup>to-be-exposed film, on a sample stage of an aligner having a masking blade;

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mounting, on a mask support of said aligner, a semitransparent phase shifting mask including (a) a first semitransparent phase shifting pattern for formation of a semiconductor device having a semitransparent phase shifting film, said phase shifting film being formed at a predetermined position on a photomask substrate and having a transmittance with respect to exposure light not higher than 25% and (b) a light shielding area provided at a peripheral edge portion of said first semitransparent phase shifting pattern and serving to make an intensity of light having passed through said light shielding area not larger than 0.05, as measured on said to-be-exposed film;

exposing a first area of said to-be-exposed film by use of said semitransparent phase shifting mask;

moving said sample stage in a horizontal direction;

exposing a second area, different from said first area, of said to-be-exposed film by use of said semitransparent phase shifting mask.

<sup>34</sup>  
D' <sup>33</sup>  
--43. A method according to claim 42, said light shielding area includes a second semitransparent phase shifting pattern having a semitransparent phase shifting portion and a transparent portion, ~~said second semitransparent phase shifting pattern being comprised of a pattern beyond a critical resolution of an exposure apparatus.~~

<sup>35</sup>  
--44. A method according to claim <sup>34</sup>43, wherein a ratio  $\alpha$  of an area of said transparent portion to an area of said semitransparent phase shifting portion is defined as  $\alpha = \beta\sqrt{T}$ , where T represents a transmittance of said semitransparent phase shifting portion, and  $\beta$  represents a value in a range  $0.5 \leq \beta \leq 2.0$ .

<sup>36</sup>  
--45. A method according to claim <sup>34</sup>43, wherein a light shielding portion is provided within a region for said first semitransparent phase shifting pattern, said light shielding portion serving to make an intensity of light having passed through said light shielding portion smaller than an intensity of light having passed through said semitransparent phase shifting film, as measured on a to-be-exposed target.

<sup>37</sup>  
--46. A method according to claim <sup>34</sup>43, wherein a transparent pattern is provided within said light shielding area to be transferred onto said to-be-exposed film.--